

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of:	)	Group Art Unit:
	)	
Tsutomu IMOTO	)	Examiner:
	)	
Application No.	)	
	)	
Filed:	)	
	)	
For: Method of Fabricating Semiconductor	)	
Device Having Semiconductor	)	
Resistance Element (as amended	)	
herein)	)	

Mail Stop Patent Application  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**PRELIMINARY AMENDMENT PURSUANT TO 1.53(b)**

Dear Sir:

Prior to examining the above-identified application, please amend the application as follows: